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Relevance scale **1 Parameterized object sensitivity for points-to and side-effect analyses for Java**

Ana Milanova, Atanas Rountev, Barbara G. Ryder

July 2002 **ACM SIGSOFT Software Engineering Notes , Proceedings of the international symposium on Software testing and analysis**, Volume 27 Issue 4Full text available:  pdf(207.18 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

The goal of *points-to analysis* for Java is to determine the set of objects pointed to by a reference variable or a reference object field. Improving the precision of practical points-to analysis is important because points-to information has a wide variety of client applications in optimizing compilers and software engineering tools. In this paper we present *object sensitivity*, a new form of context sensitivity for flow-insensitive points-to analysis for Java. The key idea of our ap ...

2 Comparing flow and context sensitivity on the modification-side-effects problem

Philip A. Stocks, Barbara G. Ryder, William A. Landi, Sean Zhang

March 1998 **ACM SIGSOFT Software Engineering Notes , Proceedings of ACM SIGSOFT international symposium on Software testing and analysis**, Volume 23 Issue 2Full text available:  pdf(1.39 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Precision and scalability are two desirable, yet often conflicting, features of data-flow analyses. This paper reports on a case study of the modification-side-effects problem for C in the presence of pointers from the perspective of contrasting the flow and context sensitivity of the solution procedure with respect to precision and scalability. The results show that the cost of precision of flow- and context-sensitive analysis is not always prohibitive, and that the precision of flow- and cont ...

Keywords: context sensitivity, empirical study, flow sensitivity, interprocedual data-flow analysis, modification side effects, pointer aliasing

3 Accurate binding-time analysis for imperative languages: flow, context, and return sensitivity

Luke Hornof, Jacques Noyé

December 1997 **ACM SIGPLAN Notices , Proceedings of the 1997 ACM SIGPLAN symposium on Partial evaluation and semantics-based program manipulation**, Volume 32 Issue 12Full text available:  pdf(962.20 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Since a binding-time analysis determines how an off-line partial evaluator will specialize a

program, the accuracy of the binding-time information directly determines the degree of specialization. We have designed and implemented a binding-time analysis for an imperative language, and integrated it into our partial evaluator for C, called *Tempo* [9]. This binding-time analysis includes a number of new features, not available in any existing partial evaluator for an imperative language, which are ...

4 Exploiting hardware performance counters with flow and context sensitive profiling

Glenn Ammons, Thomas Ball, James R. Larus

May 1997 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1997 conference on Programming language design and implementation**, Volume 32 Issue 5

Full text available:  pdf(1.67 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A program profile attributes run-time costs to portions of a program's execution. Most profiling systems suffer from two major deficiencies: first, they only apportion simple metrics, such as execution frequency or elapsed time to static, syntactic units, such as procedures or statements; second, they aggressively reduce the volume of information collected and reported, although aggregation can hide striking differences in program behavior. This paper addresses both concerns by exploiting the har ...

5 Optimization techniques for high-performance digital circuits

Chandu Visweswarah

November 1997 **Proceedings of the 1997 IEEE/ACM international conference on Computer-aided design**

Full text available:  pdf(190.24 KB)  Publisher Site Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The relentless push for high performance in custom digital circuits has led to renewed emphasis on circuit optimization or tuning. The parameters of the optimization are typically transistor and interconnect sizes. The design metrics are not just delay, transition times, power and area, but also signal integrity and manufacturability. This tutorial paper discusses some of the recently proposed methods of circuit optimization, with an emphasis on practical application and methodology impact. Circ ...

Keywords: nonlinear optimization, circuit tuning, gradients, adjoints

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